

**SILICON VALLEY IP GROUP, PC**  
95 S. MARKET STREET, SUITE 420  
SAN JOSE, CA 95113

TELEPHONE: (408) 971-2573  
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JUN 04 2004

**OFFICIAL**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Title: Hardware Accelerator for an Object-Oriented Programming Language  
Application No. 09/886,167  
Filing Date: 06/20/01  
Assignee: nVidia Corporation

Dear Sir:

Please enter the enclosed Revocation and Power of Attorney into the file of the referenced application. Further attached is a copy of an assignment submitted for recordation showing that the above assignee is the owner of the entire interest of the above-identified patent application or patent.

Very truly yours,  
Silicon Valley IP Group, PC

Kevin J. Zilka  
Reg. No. 41,429

Enclosure

CERTIFICATE OF MAILING

I do hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postage prepaid, in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date set forth below.

Melissa D. Orvis  
Melissa D. Orvis

4/30/04  
Date

Page 1 of 3

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Title: Hardware Accelerator for an Object-Oriented Programming Language

Patent No. 6,330,659

Issue Date: 12/11/01

Assignee: nVidia Corporation

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Title: Multiple Network Protocol Encoder/Decoder and Data Processor

Patent No. 6,034,963

Issue Date: 03/07/00

Assignee: nVidia Corporation

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Title: Multiple Network Encoder/Decoder and Data Processor

Application No. 10/093,340

Filing Date: 03/06/02

Assignee: nVidia Corporation

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Title: Hardware Accelerator for an Object-Oriented Programming Language

Application No. 09/886,167

Filing Date: 06/20/01

Assignee: nVidia Corporation

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Title: TCP/IP/PPP Modem

Application No. 09/321,902

Filing Date: 05/28/99

Assignee: nVidia Corporation

---

Title: TCP/IP/PPP Modem

Application No. 09/470,479

Filing Date: 12/22/99

Assignee: nVidia Corporation

---

Title: Internet Jack

Application No. 10/049,972

Filing Date: 08/16/00

Assignee: nVidia Corporation

---

Title: Internet Modem Streaming Socket Method

Application No. 09/965,952

Filing Date: 09/27/01

Assignee: nVidia Corporation

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Title: Communications Processor

Application No. 10/470,365

Filing Date: 01/25/02

Assignee: nVidia Corporation

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Page 2 of 3

Title: Giga Byte Ethernet Adaptor  
Application No. 10/131,118  
Filing Date: 04/23/02  
Assignee: nVidia Corporation

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Title: Gigabit Ethernet Adapter Supporting the iSCSI and IPSEC Protocols  
Application No. 10/456,871  
Filing Date: 06/05/03  
Assignee: nVidia Corporation

---

Title: Connection Management System and Method for a Transport Offload Engine  
Application No. 10/742,358  
Filing Date: 12/19/03  
Assignee: nVidia Corporation

---

Title: Retransmission System and Method for a Transport Offload Engine  
Application No. 10/741,978  
Filing Date: 12/19/03  
Assignee: nVidia Corporation

---

Title: Integrated Policy Checking System and Method  
Application No. 10/741,972  
Filing Date: 12/19/03  
Assignee: nVidia Corporation

---

Title: Sequence Tagging System and Method for Transport Offload Engine Data Lists  
Application No. 10/741,128  
Filing Date: 12/19/03  
Assignee: nVidia Corporation

---

Title: System and Method for Using Metadata in the Context of a Transport Offload Engine  
Application No. 10/742,352  
Filing Date: 12/19/03  
Assignee: nVidia Corporation

---

Title: System and Method for Receiving of iSCSI Protocol Data Units  
Application No. 10/741,681  
Filing Date: 12/19/03  
Assignee: nVidia Corporation

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Title: System and Method for Insertion of Markers Into a Data Stream  
Application No. 10/783,766  
Filing Date: 02/20/04  
Assignee: nVidia Corporation

---

Title: System and Method for Generating 128-bit Cyclic Redundancy Check Values with 32-bit Granularity  
Application No. 10/783,345  
Filing Date: 02/20/04  
Assignee: nVidia Corporation

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Page 3 of 3

Title: Method and Apparatus for Accessing and Maintaining Socket Information for High Speed Network Connections  
Attorney Docket # 60100-0011  
Application No. Unassigned  
Filing Date: 04/12/04  
Inventors: John S. Minami, Robin Y. Uyeshiro, Thien E. Ooi, and Murray Wallace  
Assignee: nVidia Corporation

**REVOCATION OF PRIOR POWERS OF ATTORNEY  
AND GRANT OF NEW POWER OF ATTORNEY**

Commissioner of Patents and Trademarks  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

As assignee of record of the entire interest of the above-identified patent applications(s) and/or patent(s), all powers of attorney previously given are hereby revoked and the following agents/attorneys are hereby appointed to prosecute and transact all business in the U.S. Patent and Trademark Office connected therewith.

Kevin J. Zilka (Reg. No. 41,429);  
Dominic M. Kotab (Reg. No. 42,762);  
Ronald B. Feece (Reg. No. 46,327);  
Richard B. Domingo (Reg. No. 36,784);  
Stephen H. Pettigrew (Reg. No. 35,978);  
Paul D. Carmichael (Reg. No. 18,679); and  
Joseph Vo (Reg. No. 43,971).

Please send all correspondence for these applications(s) and/or patent(s) as follows:

Cust. ID #: \*28875\*  
Silicon Valley IP Group, PC  
P.O. Box 721120  
San Jose, California 95172-1120

Please direct any calls to Kevin Zilka at (408) 505-5100.

Date 4/30/04

Richard B. Domingo

Name: Richard B. Domingo

Title: Director of Intellectual Property  
nVidia Corporation

TOTAL P.04

# **ASSIGNMENT RECORDATION COVER SHEET -PATENTS ONLY-**

To: Honorable Commissioner of Patents and Trademarks:

Please record the attached original document(s) or copy thereof.

1. Name of conveying party(ies)  
a) iReady Corporation  
2933 Bunker Hill Lane, Suite 202  
Santa Clara, California 95054-1141
2. Name and address of receiving party(ies):  
a) Name: NVIDIA Corporation  
Address: 2701 San Tomas Expressway  
Santa Clara, CA 95050

3. Nature of conveyance  

|  |  |
|--|--|
| <input checked="" type="checkbox"/> Assignment | <input type="checkbox"/> Merger            |
| <input type="checkbox"/> Security Agreement    | <input type="checkbox"/> Change of Name    |
| <input type="checkbox"/> Other _____           | <input type="checkbox"/> License Agreement |

Execution Date: April 21, 2004

4. Application Number(s) or Patent Number(s):

## **U.S. PATENTS**

| Patent No. | Issue Date | Title  |
|------------|------------|--|
| 6,330,659  | 12/11/01   | Hardware Accelerator for an Object-Oriented Programming Language |
| 6,034,963  | 03/07/00   | Multiple Network Protocol Encoder/Decoder and Data Processor     |

## **U.S. PATENT APPLICATIONS**

| Application No. | Application Date | Title   |
|-----------------|------------------|---|
| 10/093,340      | 03/06/02         | Multiple Network Encoder/Decoder and Data Processor               |
| 09/886,167      | 06/20/01         | Hardware Accelerator for an Object-Oriented Programming Language  |
| 09/321,902      | 05/28/99         | TCP/IP/PPP Modem  |
| 09/470,479      | 12/22/99         | TCP/IP/PPP Modem  |
| 10/049,972      | 08/16/00         | Internet Jack   |
| 09/965,952      | 09/27/01         | Internet Modem Streaming Socket Method                            |
| 10/470,365      | 01/25/02         | Communications Processor  |
| 10/131,118      | 04/23/02         | Giga Byte Ethernet Adaptor  |
| 10/456,871      | 06/05/03         | Gigabit Ethernet Adapter Supporting the iSCSI and IPSEC Protocols |
| 10/742,358      | 12/19/03         | Connection Management System and Method for a Transport Offload   |

Attorney Docket No. NVIDG000

(Revised 01/96)

| Application No. | Application Date | Title  |
|-----------------|------------------|--|
|                 |                  | Engine   |
| 10/741,978      | 12/19/03         | Retransmission System and Method for a Transport Offload Engine  |
| 10/741,972      | 12/19/03         | Integrated Policy Checking System and Method   |
| 10/741,128      | 12/19/03         | Sequence Tagging System and Method for Transport Offload Engine Data Lists                               |
| 10/742,352      | 12/19/03         | System and Method for Using Metadata in the Context of a Transport Offload Engine                        |
| 10/741,681      | 12/19/03         | System and Method for Receiving of iSCSI Protocol Data Units   |
| 10/783,766      | 02/20/04         | System and Method for Insertion of Markers Into a Data Stream  |
| 10/783,345      | 02/20/04         | System and Method for Generating 128-bit Cyclic Redundancy Check Values with 32-bit Granularity          |
| 10/822,896      | 4/12/04          | Method and Apparatus for Accessing and Maintaining Socket Information for High Speed Network Connections |

5. Please send all correspondence concerning this (these) documents to:

Silicon Valley IP Group, PC  
P.O. Box 721120  
San Jose, CA 95172-1120  
Tel. No.: (408) 971-2573

6. Total number of applications and patents involved: 20

7. Total fee (37 CFR 3.41): \$800.00



Enclosed



Authorized to be charged to Deposit Account No. 50-1351  
(Order No. \_\_\_\_\_)

8. To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Date: \_\_\_\_\_

04/30/04

Kevin J. Zilka  
Registration No. 41,429

FORM OF  
PATENT ASSIGNMENT  
(for U.S. Patents and Patent Applications)

WHEREAS iReady Corporation, a Delaware corporation, having a principal place of business at 2933 Bunker Hill Lane, Suite 202 Santa Clara, CA 95054-1141 ("Assignor") is an owner of the patents and patent applications listed on Attachment A (collectively, the "Patents") and the inventions set forth therein (the "Inventions"); and

WHEREAS, Assignor desires to sell, and nVidia Corporation, a Delaware corporation, having a principal place of business at 2701 San Tomas Expressway, Santa Clara, CA 95050 ("Assignee"), desires to acquire, all of Assignor's right, title and interest in and to the Patents and Inventions pursuant to Section 1.1(b) of the Asset Purchase Agreement by and between Assignor and Assignee dated as of April 15, 2004 (the "Purchase Agreement");

NOW, THEREFORE, for good and sufficient consideration, the receipt of which is hereby acknowledged, Assignor does hereby sell, assign, and transfer, to the Assignee, its successors, legal representatives, and assigns, the Assignor's entire right, title, and interest in:

- (a) the Inventions;
- (b) the Patents;
- (c) the right to file applications for patent of the United States or other countries on the Inventions, including all rights under the Hague Convention, the Paris Convention for the Protection of Industrial Property, and under the Patent Cooperation Treaty;
- (d) any provisional or other right to recover damages, including royalties, for prior infringement of any Patent; and
- (e) any patents of the United States or other countries that may be granted for or on any patent application included in the Patents or identified in clause (c) above, including continuation, divisional, continuation-in-part, any reissues and extensions of any such application.

The above-granted rights, titles, and interests are to be held and enjoyed by the Assignee, for its own use and behalf and the use and behalf of its successors, legal representatives, and assigns, as fully and entirely as the same would have been held and enjoyed by the Assignor had this sale and assignment not been made.

The Assignor hereby covenants and agrees to and with the Assignee and its successors, legal representatives, and assigns, that the Assignor will sign all papers and documents, take all lawful oaths, and do all acts necessary or required to be done in connection with any and all proceedings for the procurement, maintenance, enforcement and defense of the Patents, including interference proceedings, without charge to the Assignee or its successors, legal representatives, and assigns, but at the cost and expense of the Assignee and its successors, legal representatives, and assigns.

In the event of any conflict between this Patent Assignment and the Purchase Agreement, the Purchase Agreement will control. Nothing in this Patent Assignment should be deemed to amend or modify in any way any of the terms and conditions of the Purchase Agreement or any rights or obligations of the parties thereto.

1.

458497 v1/HN  
9TS1011.DOC

The Assignor hereby requests the Commissioner of Patents to issue said patents of the United States to the Assignee for the sole use and behalf of the Assignee, its successors, legal representatives, and assigns.

IN WITNESS WHEREOF, the undersigned has executed this assignment as of April 21, 2004.

IREADY CORPORATION  
A DELAWARE CORPORATION

By: [Signature]  
Name: Steve Bach  
Title: CFO

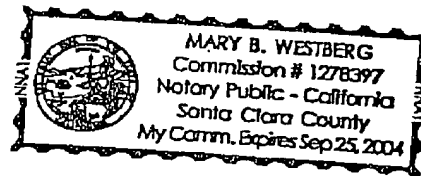
State of California }  
County of: Santa Clara }

SS. Assignment Document Return Address:  
Attention: William McGrath  
NVIDIA Corporation  
2701 San Tomas Expressway  
Santa Clara, CA 95050  
(408) 486-2941

On April 21, 2004, before me, Mary B. Westberg,  
the undersigned Notary Public, personally appeared Steve Bach ☒ personally known to me  
or ☒ proved to me on the basis of satisfactory evidence to be the person whose name is subscribed to  
the within instrument and acknowledged that he executed the same in his authorized capacity, and that by  
his signature on the instrument the person, or the entity upon behalf of which the person acted, executed  
the instrument.

WITNESS my hand and official seal.

Mary B. Westberg  
Notary's Signature





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IREADY CORPORATION  
A DELAWARE CORPORATION

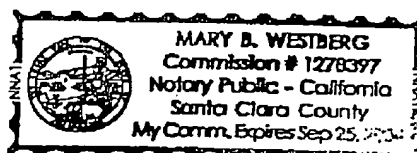
By: Steve Bault  
Name: STEVE BAULT  
Title: CFO

State of California }  
County of: Santa Clara } SS. Assignment Document Return Address:  
Attention: William McGrath  
NVIDIA Corporation  
2701 San Tomas Expressway  
Santa Clara, CA 95050  
(408) 486-2941

On April 21, 2004, before me, Mary B. Westberg,  
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or ☒ proved to me on the basis of satisfactory evidence to be the person whose name is subscribed to  
the within instrument and acknowledged that he executed the same in his authorized capacity, and that by  
his signature on the instrument the person, or the entity upon behalf of which the person acted, executed  
the instrument.

WITNESS my hand and official seal.

Max Buesch  
Notary's Signature



Signature Page – Patent Assignment

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the instrument.

WITNESS my hand and official seal.

[Signature]  
Notary's Signature



## ATTACHMENT A

## U.S. PATENTS

| Patent No. | Issue Date | Title  | Inventors |
|------------|------------|--|-----------|
| 6,330,659  | 12/11/01   | Hardware Accelerator for an Object-Oriented Programming Language |           |
| 6,034,963  | 03/07/00   | Multiple Network Protocol Encoder/Decoder and Data Processor     |           |

## U.S. PATENT APPLICATIONS

| Application No/<br>Attorney<br>Docket # | Application Date | Title   | Inventors |
|---|------------------|---|-----------|
| 10/093,340                              | 03/06/02         | Multiple Network Encoder/Decoder and Data Processor                               |           |
| 09/886,167                              | 06/20/01         | Hardware Accelerator for an Object-Oriented Programming Language                  |           |
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| 09/470,479                              | 12/22/99         | TCP/IP/PPP Modem  |           |
| 10/049,972                              | 08/16/00         | Internet Jack   |           |
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| 10/456,871                              | 06/05/03         | Gigabit Ethernet Adapter Supporting the iSCSI and IPSEC Protocols                 |           |
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| 10/741,978                              | 12/19/03         | Retransmission System and Method for a Transport Offload Engine                   |           |
| 10/741,972                              | 12/19/03         | Integrated Policy Checking System and Method                                      |           |
| 10/741,128                              | 12/19/03         | Sequence Tagging System and Method for Transport Offload Engine Data Lists        |           |
| 10/742,352                              | 12/19/03         | System and Method for Using Metadata in the Context of a Transport Offload Engine |           |
| 10/741,681                              | 12/19/03         | System and Method for Receiving of iSCSI Protocol Data Units                      |           |

A-1.

458497 v1/HN  
9TS1011.DOC

| Application<br>No./<br>Attorney<br>Docket # | Application Date | Title  | Inventors   |
|---|------------------|--|---|
| 10/783,766                                  | 02/20/04         | System and Method for Insertion of<br>Markers Into a Data Stream   |   |
| 10/783,345                                  | 02/20/04         | System and Method for Generating<br>128-bit Cyclic Redundancy Check<br>Values with 32-bit Granularity          |   |
| Docket # 60100-<br>0011                     | 4/12/04          | Method and Apparatus for Accessing<br>and Maintaining Socket Information<br>for High Speed Network Connections | John S. Minami<br>Robin Y. Uyeshiro<br>Thien E. Ooi<br>Murray Wallace |

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